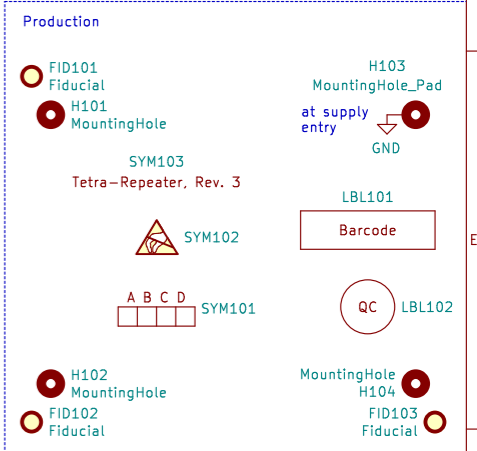


**Notes**  
 Unless otherwise noted:  
 All resistors 0603, 1/40 W  
 All ceramic capacitors MLCC, X7R, 0603, 50 V  
 For the sake of clarity, all test provisions have been omitted  
 Do not use as a template - incorrect!

JTAG: Debugging and storing code via FPGA into FLASH  
 Pull-ups in FPGA



<b>Project Title:</b> Design Review Day	
<b>Sheet Title:</b> Repeater	<b>Rev:</b> 3
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